CLAIMS

What is claimed is:

1. Storage control for implementing switch commands for access to storage cells for writing and reading data, which storage control receives control commands allocated to the data, contains for at least one storage type a command set with command sequences of individual switch commands to be processed in a predetermined succession and activated by the control commands,

which activates one after the other the switch commands of a command sequence corresponding to the current control command, and

activates the current switch command at a predetermined time later than the preceding switch command,

characterized in that

dependent on the previous switch command and the current control command the current switch command is activated, and

that information items allocated to the storage cells are capable of being supplied to the storage control, which information items indicate the predetermined time mentioned.

2. Storage control according to claim 1, **characterized** in that for every switch command a reference value is stored in a first register.

3. Storage control according to claim 2, **characterized** in that a counter counts the impulses of an impulse series after the activating of the switch command.

4. Storage control according to claim 3, **characterized** in that a counter reading is to be compared with the reference value, and that a first signal is created, if the counter reading is the same as the reference value.

5. Storage control according to claim 4, **characterized** in that the first signal is apprehended, and that on apprehending the first signal the next switch command is activated.

6. Storage control according to claim 5, **characterized** by at least one switch command that is already activated before apprehending the first signal.

7. Storage control according to claim 2, **characterized** in that the reference values for the switch commands are written in an installation process into the first register.

8. Storage control according to claim 2, **characterized** in that the first register also contains a value for the time after which the storage content is to be refreshed.

9. Storage control according to claim 4, **characterized** by a device, which creates a second signal, if the current control command belongs to a new command sequence.

10. Storage control according to claim 1, **characterized** in that operational control commands are created by the storage control for processing of individual switch commands of a command sequence in the predetermined succession.

11. Storage control according to claim 2, **characterized** in that the activating commands are stored in a second register (30).

12. Storage control according to claim 11, **characterized** in that the switch commands are stored in a third register (32).

13. Storage controls according to claim 12, **characterized** in that the third register (32) is a write/read memory or a programmable read-only-memory, into which the switch commands are written in an initializing process.

14. Storage control according to claim 1, **characterized** in that a third signal is created, if the current command sequence relates to storage cells of the same line of the memory as the previous command sequence.

15. Storage control according to claim 1, **characterized** in that the command set is determined for each respective storage type by means of information items allocated to the storage cells.

16. Storage control according to claim 14, **characterized** in that a fourth signal is created, when the writing or reading is concluded.

17. Storage control according to claim 16, **characterized** in that the fourth signal is given back together with the source address for the access after the conclusion.

18. Storage control according to claim 16, **characterized** in that a fifth signal is created if the current switch command is the first switch command of a new command sequence.

19. Storage control according to claim 18, **characterized** in that the information items allocated to the storage cells also give the time, after which after activating the first switch command of each new command sequence, the writing or reading respectively is concluded.